

		L#		Search Text			
	BRS	L2	209	(discharge adj (lamp or lighting)) and (AC or (alternating adj current)) and (DC or (direct adj current)) and conver\$4 and (power near3 regulat\$3) and inverter)USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:51	0
	BRS	L3	95	2 and (square adj wave)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:51	0
	BRS	I.A	65	3 and impedance	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 16:30	0
	BRS	L6	4	5 and microprocessor	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 16:31	0
	BRS	L7	3	6 and program\$5	USPAT, US-PGPUB, EPO, JPO; DERWENT	2004/09/02 16:32	0
	BRS	L5	65	4 and control\$4	USPAT, US-PGPUB, EPO, JPO, DERWENT	2004/09/02 17:52	o
	BRS	L11	366	(discharge adj (lamp or lighting)) and (AC or (alternating adj current)) and (DC or (direct adj current)) and conver\$4 and (power near3 regulat\$3)	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:51	0
	BRS	L12	128	11 and (square adj wave)	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:52	0
	BRS	L13	90	12 and resonan\$3 and inductor and capacitor	USPAT, US-PGPUB; EPO, JPO; DERWENT	2004/09/02 17:52	0
0 1	BRS	L14	90	13 and control\$4	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:53	0
	BRS	L16	3	14 and (external adj source)	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:53	0
2 4	BRS	L15	16	14 and battery	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 17:56	0